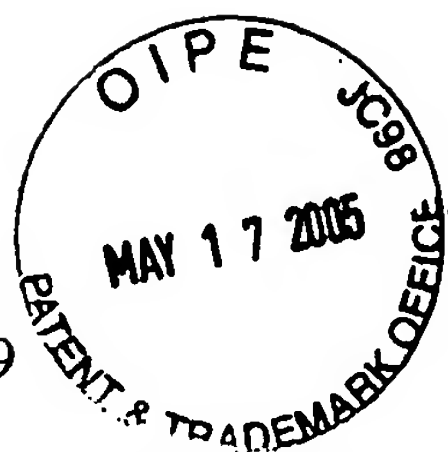


Docket No.: 056937-0089



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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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| In re Application of | : | Customer Number: 20277 |
| Kenichiro UDA | : | Confirmation Number: 3242 |
| Application No.: 10/658,195 | : | Group Art Unit: 2818 |
| Filed: September 10, 2003 | : | Examiner: Not yet assigned |
| For: STRUCTURE OF POWER SUPPLY PATH UTILIZED IN DESIGN OF INTEGRATED CIRCUIT | | |

INFORMATION DISCLOSURE STATEMENT

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the references listed on the attached form PTO-1449. It is respectfully requested that the references be expressly considered during the prosecution of this application, and that the references be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

Each non-English language reference was first cited in a corresponding foreign application search report or office action and its relevance discussed therein. A copy of the

10/658,195

foreign search report or office action, together with an English language version thereof, is attached for the Examiner's information.

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

McDERMOTT WILL & EMERY LLP


Michael E. Fogarty
Registration No. 36,139

600 13th Street, N.W.
Washington, DC 20005-3096
Phone: 202.756.8000 MEF:aph
Facsimile: 202.756.8087
Date: May 17, 2005

**Please recognize our Customer No. 20277
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SHEET 1 OF 1

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| INFORMATION DISCLOSURE CITATION IN AN APPLICATION (PTO-1449) | | | | ATTY. DOCKET NO. 056937-0089 | | SERIAL NO. 10/658,195 | | |
| | | | | APPLICANT Kenichiro UDA | | | | |
| | | | | FILING DATE September 10, 2003 | | GROUP 2818 | | |
| U.S. PATENT DOCUMENTS | | | | | | | | |
| EXAMINER'S INITIALS | CITE NO. | Document Number Number-Kind Code ² (if known) | Publication Date MM-DD-YYYY | Name of Patentee or Applicant of Cited Document | | Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear | | |
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| | | JP 07-078877 | 3-20-1995 | FUJITSU LTD FUJITSU VLSI LTD | | | English Abstract | |
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.